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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/019,526	12/27/2001	Reinhold Noe	112740-387	2104
29177 7	590 05/17/2005		EXAMINER	
BELL, BOYD & LLOYD, LLC P. O. BOX 1135			LEUNG, CHRISTINA Y	
CHICAGO, IL 60690-1135			ART UNIT	PAPER NUMBER
			2633	

DATE MAILED: 05/17/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

	Application No.	Applicant(s)				
	10/019,526	NOE, REINHOLD				
Office Action Summary	Examiner	Art Unit				
	Christina Y. Leung	2633				
The MAILING DATE of this communication app	ears on the cover sheet with the c	orrespondence address				
Period for Reply						
A SHORTENED STATUTORY PERIOD FOR REPLY THE MAILING DATE OF THIS COMMUNICATION. - Extensions of time may be available under the provisions of 37 CFR 1.13 after SIX (6) MONTHS from the mailing date of this communication. - If the period for reply specified above is less than thirty (30) days, a reply - If NO period for reply is specified above, the maximum statutory period w - Failure to reply within the set or extended period for reply will, by statute, Any reply received by the Office later than three months after the mailing earned patent term adjustment. See 37 CFR 1.704(b).	66(a). In no event, however, may a reply be tim within the statutory minimum of thirty (30) days ill apply and will expire SIX (6) MONTHS from to cause the application to become ABANDONE	ely filed will be considered timely. the mailing date of this communication. (35 U.S.C. § 133).				
Status						
1) Responsive to communication(s) filed on 27 De	ecember 2001.					
	action is non-final.					
3) Since this application is in condition for allowar						
closed in accordance with the practice under E	closed in accordance with the practice under Ex parte Quayle, 1935 C.D. 11, 453 O.G. 213.					
Disposition of Claims						
4) Claim(s) 14-28 is/are pending in the application	1.					
4a) Of the above claim(s) is/are withdrawn from consideration.						
5) Claim(s) is/are allowed.						
6)⊠ Claim(s) <u>14,15,17-21,27 and 28</u> is/are rejected.						
7)⊠ Claim(s) <u>16 and 22-26</u> is/are objected to.						
8) Claim(s) are subject to restriction and/or	8) Claim(s) are subject to restriction and/or election requirement.					
Application Papers		•				
9) The specification is objected to by the Examiner.						
10)⊠ The drawing(s) filed on 27 December 2001 is/a	re: a)⊠ accepted or b)⊡ object	ed to by the Examiner.				
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).						
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).						
11) The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.						
Priority under 35 U.S.C. § 119						
 12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f). a) All b) Some * c) None of: 1. Certified copies of the priority documents have been received. 						
2. Certified copies of the priority documents have been received in Application No						
3. Copies of the certified copies of the priority documents have been received in this National Stage						
application from the International Bureau (PCT Rule 17.2(a)).						
* See the attached detailed Office action for a list of the certified copies not received.						
Attachment(s) 1) Notice of References Cited (PTO-892) 4) Interview Summary (PTO-413)						
1) Notice of References Cited (PTO-892) 2) Notice of Draftsperson's Patent Drawing Review (PTO-948)	Paper No(s)/Mail Da	ate				
3) Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08) Paper No(s)/Mail Date 12-27-01.	5) ☐ Notice of Informal P 6) ☐ Other:	atent Application (PTO-152)				

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DETAILED ACTION

Introductory comments

1. Applicants' preliminary amendment canceling claims 1-13 and adding claims 14-28, filed on 27 December 2001 with the original application, is acknowledged.

Claim Rejections - 35 USC § 112

- 2. The following is a quotation of the second paragraph of 35 U.S.C. 112:
 - The specification shall conclude with one or more claims particularly pointing out and distinctly claiming the subject matter which the applicant regards as his invention.
- 3. Claims 19 and 20 are rejected under 35 U.S.C. 112, second paragraph, as being indefinite for failing to particularly point out and distinctly claim the subject matter which applicant regards as the invention.

Claims 19 and 20 both recite "one of the delay lines" in line 3 of the claims. There is insufficient antecedent basis for this limitation in the claims because claim 15, on which claims 19 and 20 each depend, only recites "a delay line" and does not specifically recite a plurality of delay lines. Examiner respectfully suggests that claims 19 and 20 may depend on claim 18 instead.

Claim Rejections - 35 USC § 102

4. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

- (b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.
- 5. Claims 14, 15, 17, 18, 21, and 27 are rejected under 35 U.S.C. 102(b) as being anticipated by Jenson (US 4,973,969 A).

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Regarding claim 14, Jenson discloses a device for evaluating an electrical baseband signal, comprising:

a device for evaluating an electrical baseband signal (Figure 2) comprising:

at least one multiplier (exclusive OR gates 40a...h) which calculates a value of an autocorrelation function of the baseband signal by multiplication of a value of the baseband signal by a delayable value of the baseband signal (column 2, lines 5-14; column 6, lines 15-54); and

an averaging device 50 for averaging the calculated value of the autocorrelation function (column 6, lines 55-56).

Regarding claim 15, Jenson disclose a delay line 30 with taps, wherein taps with different delays are respectively connected to inputs of the at least one multiplier 40a...h (column 6, lines 15-54).

Regarding claim 17, Jenson et al. disclose that two delay lines (i.e., wherein one delay line is specifically marked as delay element 30 and wherein the other delay line is the transmission line parallel to element 30) are provided which, in a region in which the two delay lines exhibit an assignment via the inputs of the at least one multiplier, are traversed in a same direction such that the delays occurring between adjacent multipliers are subtracted from one another to form a delay different between the adjacent multipliers (Figure 2).

Regarding claim 18, Jenson et al. disclose a plurality of delay lines of different lengths to whose ends inputs of multipliers are connected (Figure 2).

Regarding claim 21, Jenson discloses that the delays that occur are equidistant with a constant delay difference (column 6, lines 38-39).

Regarding claim 27, Jenson discloses that the multiplier is an EXOR gate (column 6, lines 45-48).

Claim Rejections - 35 USC § 103

- 6. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:
 - (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.
- 7. Claim 19 is rejected under 35 U.S.C. 103(a) as being unpatentable over Jenson et al. in view of Cuzin et al. (US 4,933,634 A).

Regarding claim 19, as well as the claim may be understood with regard to 35 U.S.C. 112 discussed above, Jenson et al. disclose a system as discussed above with regard to claims 15 and 18 including a delay line but do not specifically disclose detour lines. However, various ways of implementing delay lines are well known in the art, and Cuzin et al. teach changing the path length of a transmission line to create a "detour line" providing a desired delayed transmission (Figure 3B shows a line having distance x2 between elements in relation to another line having distance x1 between elements in Figure 3A). It would have been obvious to a person of ordinary skill in the art to provide detour lines as suggested by Cuzin et al. in the delay line disclosed by Jenson et al. as an engineering design choice of a way to provide a delayed transmission.

8. Claim 20 is rejected under 35 U.S.C. 103(a) as being unpatentable over Saxe et al. (US 5,144,525 A).

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Regarding claim 20, as well as the claim may be understood with regard to 35 U.S.C. 112 discussed above, Jenson et al. disclose a system as discussed above with regard to claims 15 and 18 including a delay line but do not specifically disclose buffer amplifiers. However, various ways of implementing delay lines are well known in the art, and Saxe et al. teach providing buffer amplifiers 42 in a delay line (Figure 3; column 4, lines 7-11). It would have been obvious to a person of ordinary skill in the art to provide buffer amplifiers as suggested by Saxe et al. in the delay line disclosed by Jenson et al. as an engineering design choice of a way to provide a delayed transmission.

9. Claim 28 is rejected under 35 U.S.C. 103(a) as being unpatentable over Jenson in view of Sevenhans et al. (US 5,528,637 A).

Regarding claim 28, Jenson discloses a system as discussed above with regard to claim 14 including multipliers 40a...h but does not specifically disclose that they may be Gilbert multipliers. However, various elements for providing a multiplier function in an electrical circuit are well known in the art, and Sevenhans et al. particularly teach that multipliers providing an EXOR such as already disclosed by Jenson may specifically comprise Gilbert multipliers (column 7, lines 30-34). It would have been obvious to a person of ordinary skill in the art to specifically use an Gilbert multiplier as taught by Sevenhans et al. in the system disclosed by Jenson simply as an engineering design choice of a well known way to implement the already disclosed multiplier element. Jenson already discloses that the multipliers 40a...h may be analog multipliers instead of digital EXOR gates (column 8, lines 19-20), and it would be understood in the art that Gilbert multipliers would be appropriate for usage in such an analog circuit arrangement.

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Examiner also respectfully notes that Applicants acknowledge on page 4, line 29 of the specification that various multiplier circuits may be used. The claimed differences exist not as a result of an attempt by Applicants to solve an unknown problem but merely amount to the selection of expedients known as design choices to one of ordinary skill in the art.

Allowable Subject Matter

- 10. Claims 16 and 22-26 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.
- 11. The following is a statement of reasons for the indication of allowable subject matter:

The prior art, including Jenson et al., does not specifically disclose or fairly suggest a device for detecting polarization mode dispersion including all the limitations and elements recited in claims 23-26 (and including all the limitations of claim 14 on which the claims depend), particularly including the combination of a multiplier for calculating an autocorrelation of a baseband signal; an averaging device for averaging the autocorrelation, a PMD compensator; and a regulator.

The prior art also does not specifically disclose or fairly suggest a device for detecting polarization mode dispersion including all the limitations and elements recited in claims 16 and 22 (and including all the limitations of claims 14 and 15 on which the claims depend), particularly wherein the baseband signal runs in opposite directions such that the delays occurring between adjacent multipliers are added to from a delay difference between the adjacent multipliers or wherein a delay different is at least approximately equal to a symbol duration of the baseband signal.

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Conclusion

12. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Christina Y. Leung whose telephone number is 571-272-3023. The examiner can normally be reached on Monday to Friday, 6:30 to 3:00.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Jason Chan can be reached on 571-272-3022. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is 571-272-2600.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Christina Y Leung Christina Y Leung Patent Examiner Art Unit 2633